

Spotlight Memory, Logic at ISSCC

Some 1,300 engineers and scientists from industry and academe gathered at the Sheraton Hotel in Philadelphia last week for the annual International Solid State Circuits Conference.

Technical sessions, heavily freighted with advances in semiconductor memory and logic design, kept interest at a high level for the 3-day event.

A large contingent of engineers from Europe and Japan reported on new technology overseas. (See story, Page 52).



Veteran delegates to the annual ISSCC, Jack Kilby (right), now a consultant, and Clare Thornton, director of semiconductor devices and integrated electronics R&D at the Army Electronics Command, exchange some nostalgic thoughts before the conference luncheon.



Dennis Buss (left) branch manager for CCD signal processing and memory for Texas Instruments, enjoys a short break in sessions with Donald Gaffney, staff engineer at IBM's semiconductor facility in Burlington, Vt.



Tom Longo (right), Fairchild Camera vice-president and chief technical officer, chats in the lobby of the Sheraton Hotel with Mostek regional manager Joe Bailey (left) and Ken Davis, Mostek marketing manager.



Leslie L. Vadasz (left), vice-president of engineering at Intel and chairman of the memory sessions at the ISSCC, has a word with John H. Wuorinen, Jr., department head of the Common Systems Lab, Bell Laboratories, and technical program chairman for the conference.



Richard Petritz (right), president of New Business Ventures and conference keynoter, shares the head table with Murlan S. Corrington, chairman of the ISSCC executive committee and staff member at RCA's Advanced Technology Laboratories.



Breaking bread during the conference luncheon are J. Reese Brown, Jr. (left), senior staff engineer, Burroughs Corp., and John D. Husher, managing director for bipolar memories at Advanced Micro Devices.